INFORMATION DISCLOSURE LIST
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		ATTY. DOCKET NO.	SERIAL NO.
U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		B-24314C2	582,672
	APPLICANT	Joseph T. Evans, Jr. et al.	
	FILING DATE September 14, 1990		GROUP 233

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
(init)	AA	4 8 6 0 2 5 4	8/89	Pott et al.	365	145	1-27-87
	AB	4 4 7 7 8 8 6	10/84	Au	365	222	3-26-82
	AC	4 4 0 8 3 0 3	10/83	Guterman et al.	365	205	12-28-81
	AD	3 8 3 2 7 0 0	8/74	Wu et al.	340	173.2	
	AE	4 1 6 1 0 3 8	7/79	Wu	365	145	
	AF	2 9 3 8 1 9 4	5/60	Anderson	340	173	
	AG	4 8 7 3 6 6 4	10/89	Eaton, Jr.	365	145	
	AH						
	AI				RECEIVED		
	AJ				AUG 16 1991		
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FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES	NO
(init)	AL	1 5 4 4 3 1 4	4/19/79	Uchida et al. UK			NO	
	AM							
	AN							
	AO							
	AP							

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

(init)	AR	Hybrid Volatile/NonVolatile Integrated Memory Arrays, IBM Technical Disclosure Bulletin, Vol. 18, No. 5, October, 1975
	AS	
	AT	

EXAMINER	DATE CONSIDERED
Alyssa H. Bowen	9/30/91

Sheet 1 of 1

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EXAMINER *(signature)* Alyssa H. Bowl

DATE CONSIDERED 9/30/91